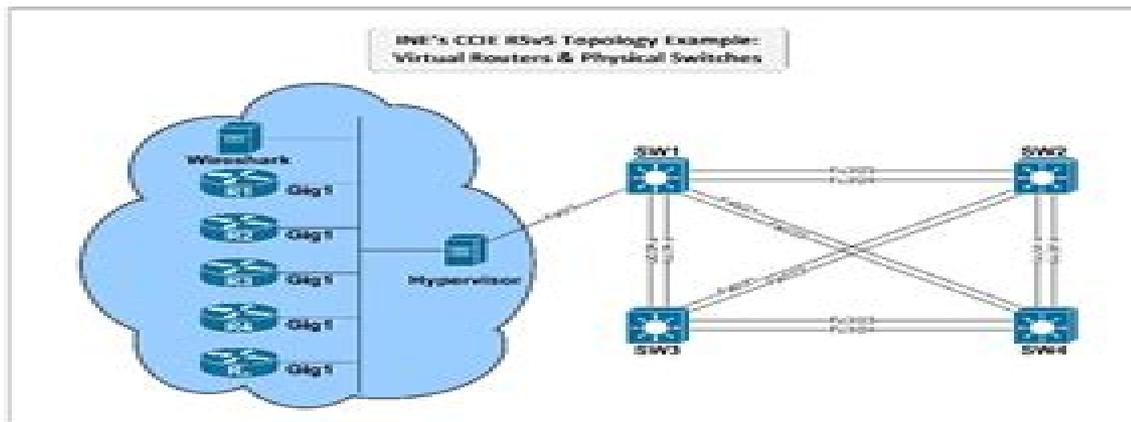


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CCIE R&S v5 Topology Diagrams & Initial Configurations

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