Memory reference Instructions

- Address part = 12 bits long.
- Addressing mode = 1 bit.
- Opcode = 3bits (from 000 to 110).

15	14	12	11	0

(Op code = 000 to 110)

Register reference Instructions

Addressing mode (1 bit) = 0.
Opcode (3bits) = 111.

15	12	11	0

(Op code = 111, I = 0)

Input-output reference Instructions

Addressing mode (1 bit) = 1.
Opcode (3bits) = 111.

15	12	11	0
		I/O operation	

(Op code = 111, I = 1)

Manufacturer Memory Reference Guide

M. A. Laughton,D.F. Warne

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