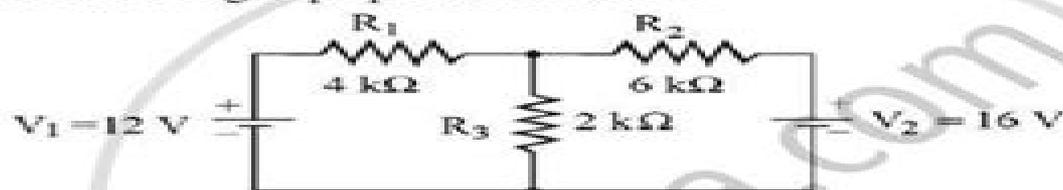


- (c) Draw the circuit diagram for Op-Amp as a subtractor. Derive an expression for its output voltage.
- (d) Explain phase shift oscillator with proper circuit diagram.
- (e) Compare BJT and FET (any four points).
- (f) Explain working of reverse biased PN junction diode with proper circuit diagram.

Q. 3. Attempt any four of the following :

[16]

- (a) Explain 'virtual ground' concept in case of Op-Amp.
- (b) Find the current flowing through resistor  $R_3$  in the following circuit using superposition theorem.

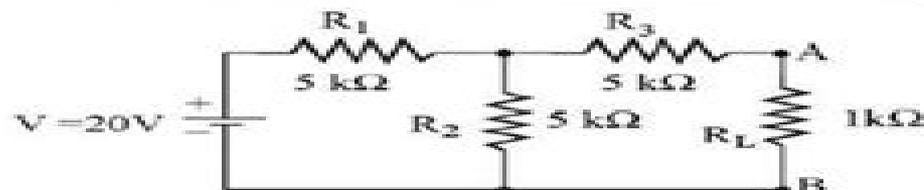


- (c) Draw and explain the block diagram of ON-line UPS.
- (d) Explain working of flat-bed scanner.
- (e) Give the ideal values of Op-Amp characteristics (any four).
- (f) Explain working principle of light emitting diode (LED) and state its any two applications.

Q. 4. Attempt any two of the following :

[16]

- (a) (i) Explain Hartley's oscillator with neat circuit diagram.
- (ii) Draw Norton's equivalent circuit for the following circuit:



# Electronics Devices And Circuits Sample Question Paper

**Yijin Wang**



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