3 HE910 module connections

3.1 PIN-OUT

PAD	Signal	I/O	Function	Type	COMMENT
USB HS 2.0 COMMUNICATION PORT					
B15	USB_D+	I/O	USB differential Data (+)		
C15	USB_D-	I/O	USB differential Data (-)		
A13	VUSB	I	Power sense for the internal USB transceiver.		
Asynchronous Serial Port (USIF0) – Prog. / Data + HW Flow Control					
N15	C103/TXD	I	Serial data input from DTE	CMOS 1.8V	
M15	C104/RXD	O	Serial data output to DTE	CMOS 1.8V	
M14	C108/DTR	I	Input for (DTR) from DTE	CMOS 1.8V	
L14	C105/RTS	I	Input for Request to send signal (RTS) from DTE	CMOS 1.8V	
P15	C106/CTS	O	Output for Clear to Send signal (CTS) to DTE	CMOS 1.8V	
N14	C109/DCD	O	Output for (DCD) to DTE	CMOS 1.8V	
P14	C107/DSR	O	Output for (DSR) to DTE	CMOS 1.8V	
R14	C125/RING	O	Output for Ring (RI) to DTE	CMOS 1.8V	
Asynchronous Auxiliary Serial Port (USIF1)					
D15	TX_AUX	O	Auxiliary UART (TX Data to DTE)	CMOS 1.8V	
E15	RX_AUX	I	Auxiliary UART (RX Data from DTE)	CMOS 1.8V	
D13	VDD_IO1	I	IO1 SUPPLY Input		This pin has to be connected to E13
E13	VIO1_1V8	O	VIO1 Supply output (1.8V)		To be used only to supply VDD_IO1
SIM card interface					
A6	SIMCLK	O	External SIM signal – Clock	1.8 / 3V	
A7	SIMRST	O	External SIM signal – Reset	1.8 / 3V	
A5	SIMIO	I/O	External SIM signal – Data I/O	1.8 / 3V	
A4	SIMIN	I	External SIM signal – Presence (active low)	CMOS 1.8	
A3	SIMVCC	-	External SIM signal – Power supply for the SIM	1.8 / 3V	
Digital Voice Interface (DVI) (See NOTE 1)					
B9	DVI_WA0	I/O	Digital Audio Interface (WA0)	CMOS 1.8V	
B6	DVI_RX	I/O	Digital Audio Interface (RX)	CMOS 1.8V	
B7	DVI_TX	I/O	Digital Audio Interface (TX)	CMOS 1.8V	
B8	DVI_CLK	I/O	Digital Audio Interface (CLK)	CMOS 1.8V	
SPI					
D15	SPI_MOSI	I	SPI MOSI	CMOS 1.8V	Shared with TX_AUX

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